



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

List of **Virtual Webinars** conducted from **08-06-2020** to **13-06-2020**

| S. NO | Date & Time | Subject | Subject Expert | Topic | Number of Students Attended |
|-------|--|-----------|--|-----------------------------------|-----------------------------|
| 1. | 10/06/2020 & 10:00 A.M to 11:00 A.M | VLSI | Mr. Rahul A Bal, CEO, Electron – Its, Pune. | VHDL | 106 |
| 2. | 11/06/2020 & 11:00 A.M to 12:30 A.M | VLSI | Dr.K.V.Ramanaiah, Professor Yogi Vemana University, Proddutur | Overview of VLSI | 109 |
| 3. | 11/06/2020 & 05:00 P.M to 07:00 P.M | Placement | Mr. K. Ranjith Kumar, Product Designer / Electrical Architect, Philips India LTD, Pune | Career Options & Opportunities | 85 |
| 4. | 12/06/2020 & 09:30 A.M to 10:30 A.M | DSP | Dr. S. Satheesh, Professor, RISE Krishna Sai Prakasam Group of Institutions, Ongole | Digital Filter Design | 107 |
| 5. | 13/06/2020 & 10:30 A.M to 11:30 A.M | EMI | Dr. P. Vinod Kumar, Associate Professor, Department of EIE, Bapatla Engineering College, Bapatla. | Transducers | 100 |

A Report of Virtual Webinar on "VHDL"

Department of Electronics and Communication Engineering, Narayana Engineering College, Nellore organized a **Virtual Webinar** on "**VHDL**" on 10-06-2020 from 08:00 A.M to 09:30 A.M for III B.Tech ECE students. **Mr. Rahul A Bal, CEO, Electron – Its, Pune.** was the resource person. A total of **106** students participated in the virtual webinar.

In this session Dr. K. Murali, Head of the Department addressed the students and introduced the resource person Mr. Rahul A Bal, CEO, Electron – Its, Pune to the students. Later the resource person enlightened the students on the different programming styles of VHDL. He explained about the Design flow of IC and also demonstrated the design of adder, logic gates, encoders, decoders etc. using VHDL.

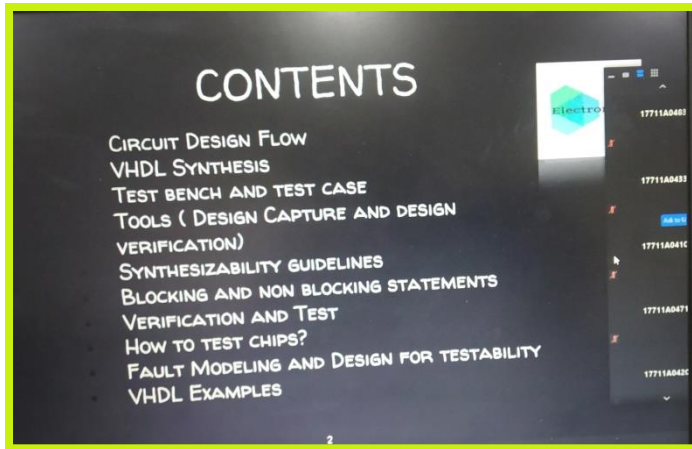


Figure: Explaining about the overview of VHDL

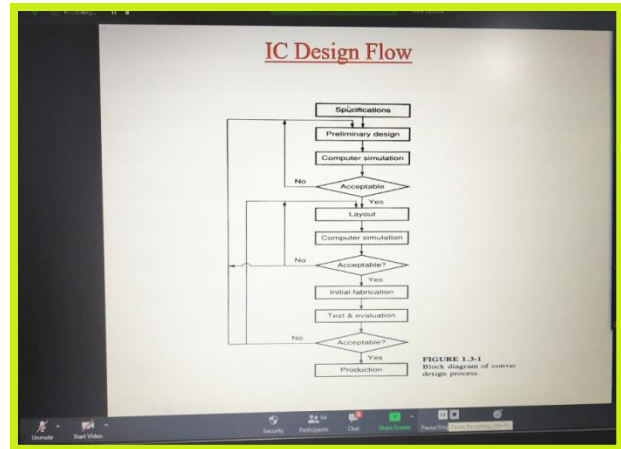


Figure: Explaining about IC Design Flow

At the end of the webinar session, the resource person clarified the queires of students and finally the session was concluded with a vote of thanks.

A Report of Virtual Webinar on "Overview of VLSI"

Department of Electronics and Communication Engineering, Narayana Engineering College, Nellore organized a **Virtual Webinar** on "**Overview of VLSI**" on 11-06-2020 from 11:00 A.M to 12:30 P.M for III B.Tech ECE students. **Dr.K.V.Ramanaiah, Professor, Yogi Vemana University, Proddutur** was the resource person. A total of **109** students participated in the virtual webinar.

In this session Dr. K. Murali, Head of the Department addressed the students and introduced the resource person Dr.K.V.Ramanaiah, Professor, Yogi Vemana University, Proddutur to the students. Later the resource person enlightened the students on the history and the overview of VLSI Technology. He explained the different phases that are involved in designing an Integrated Circuit(IC).

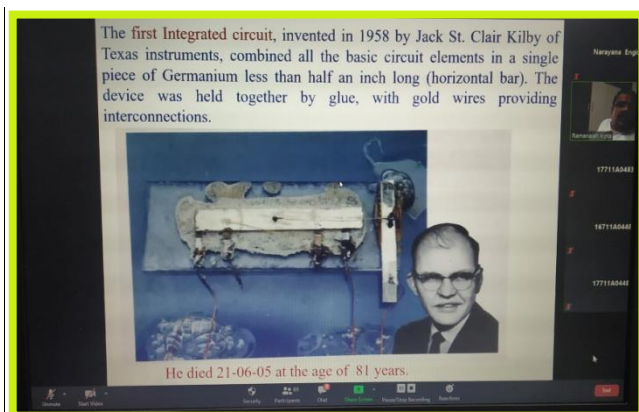


Figure: Explaining about Integrated Circuit(IC)

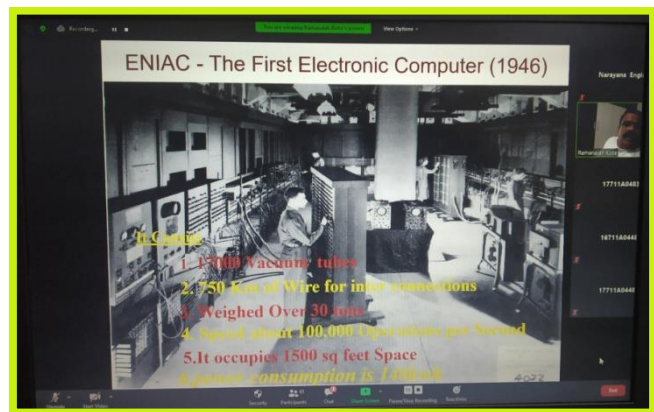


Figure: Explaining about First Electronic Computer

He also explained about different applications of VLSI technology. At the end of the webinar session, the resource person clarified the queires of students and finally the session was concluded with a vote of thanks.

A Report of Virtual Webinar on "Career Options & Opportunities"

Department of Electronics and Communication Engineering, Narayana Engineering College, Nellore organized a **Virtual Webinar** on "**Career Options & Opportunities**" on **11-06-2020** from **05:00 P.M** to **07:00 P.M** for III B.Tech ECE students. **Mr. K. Ranjith Kumar, Product Designer / Electrical Architect, Philips India LTD, Pune** was the resource person. A total of **85** students participated in the virtual webinar.

In this session Dr. K. Murali, Head of the Department addressed the students and introduced the resource person Dr Mr. K. Ranjith Kumar, Product Designer / Electrical Architect, Philips India LTD, Pune to the students. Later the resource person enriched the students on the various options and opportunities for B.Tech Graduates.

The job opportunities after B.Tech is a good option. You can enter the software industry according to your specialization provided you possess good communication skills. It is better to get job in a company through campus placements as it is difficult to get job after you are out of the college.

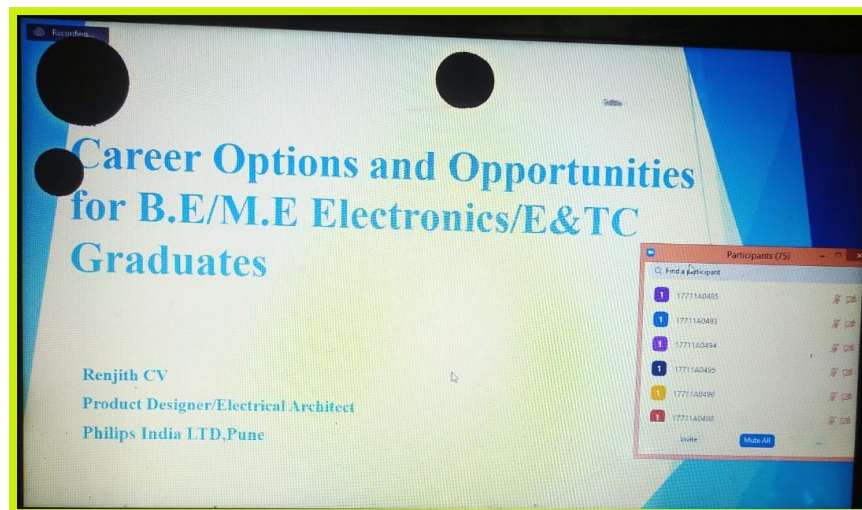


Figure: Explaining different Options & Opportunities for B.Tech Graduates.

Most of you think that it is better to stop education at graduation level and invests their efforts in getting a job. Very few are interested towards higher studies. Investing in higher education will yield long term benefits. As B.Tech is a bachelor level course in Technology, one cannot take it as the last and final qualification degree, especially in the present time when the market is full of competition all around. Mere earning a bachelor's degree cannot give you a job, in the present era. There are more applicants than the number of jobs available, and hence it is very essential for everyone to be specialized in their respective field. Doing post graduation not only gives an additional degree but also it enhances your intellectual and maturity levels. It makes you specialist in a particular area or field so that you will be suitable for Specific job.

At the end of the webinar session, the resource person clarified the queires of students and finally the session was concluded with a vote of thanks.

A Report of Virtual Webinar on "Digital Filters"

Department of Electronics and Communication Engineering, Narayana Engineering College, Nellore organized a **Virtual Webinar** on "**Digital Filters**" on 12-06-2020 from 09:30 A.M to 10:30 A.M for III B.Tech ECE students. **Dr. S. Satheesh, Professor, RISE Krishna Sai Prakasam Group of Institutions, Ongole** was the resource person. A total of **107** students participated in the virtual webinar.

In this session Dr. K. Murali, Head of the Department addressed the students and introduced the resource person Dr. S. Satheesh, Professor, RISE Krishna Sai Prakasam Group of Institutions, Ongole to the students. Later the resource person enriched the students on the basics of digital filters and the various techniques of designing digital filter. He explained about different methods that are used for designing Finite Impulse Response (FIR) and Infinite Impulse Response(IIR) filters.

Characteristics of Prototype Analog Filters

- Magnitude-squared function
- Let $H_a(j\Omega)$ be the frequency response of an analog filter

$$\frac{1}{1+\epsilon^2} \leq |H_a(j\Omega)|^2 \leq 1, \quad |\Omega| \leq \Omega_p$$
$$0 \leq |H_a(j\Omega)|^2 \leq \frac{1}{A^2}, \quad |\Omega| \geq \Omega_s$$
$$|H_a(j\Omega)|^2 = \frac{1}{1+\epsilon^2} \quad \text{at } \Omega = \Omega_p$$
$$|H_a(j\Omega)|^2 = \frac{1}{A^2} \quad \text{at } \Omega = \Omega_s$$

ϵ is a passband ripple parameter
 A is a stopband attenuation parameter
 Ω_p is the passband cutoff frequency in rad/sec

Participants (92)

Q. Find a participant

- NE Narayana Engl... (Host)
- D Dr.S.Satheesh (Co-host)
- C chandram (Co-host)
- 1 16711AD413
- 1 16711AD448
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- 1 17711AD403
- 1 17711AD404
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Figure: Explaining the characteristics of prototype analog filters.

At the end of the webinar session, the resource person clarified the queires of students and finally the session was concluded with a vote of thanks.

A Report of Virtual Webinar on "Transducers"

Department of Electronics and Communication Engineering, Narayana Engineering College, Nellore organized a **Virtual Webinar** on "**Transducers**" on 13-06-2020 from 10:30 A.M to 11:30 A.M for III B.Tech ECE students. **Dr. P. Vinod Kumar, Associate Professor, Department of EIE, Bapatla Engineering College, Bapatla.** was the resource person. A total of **100** students participated in the virtual webinar.

In this session Dr. K. Murali, Head of the Department addressed the students and introduced the resource person Dr. P. Vinod Kumar, Associate Professor, Department of EIE, Bapatla Engineering College, Bapatla to the students. Later the resource person enlightened the students on the basic definition, types and working principle of transducers.

He explained about various advantages and applications of transducers. Later he concluded that measuring any physical quantity with a electrical transducer is very easy and convenient. The Electrical transducer illustrates the concept of a measurement of any physical quantity, which can be extremely accurate. By means of a transducer, a complex electrical quantity, such as watts, can be measured at a convenient location For remote indication of watts or vars, a transducer can reduce the number of signal wires to be laid between source and indicator from as many as nine to two. Hence it can reduce the cost of a project to a large extent.

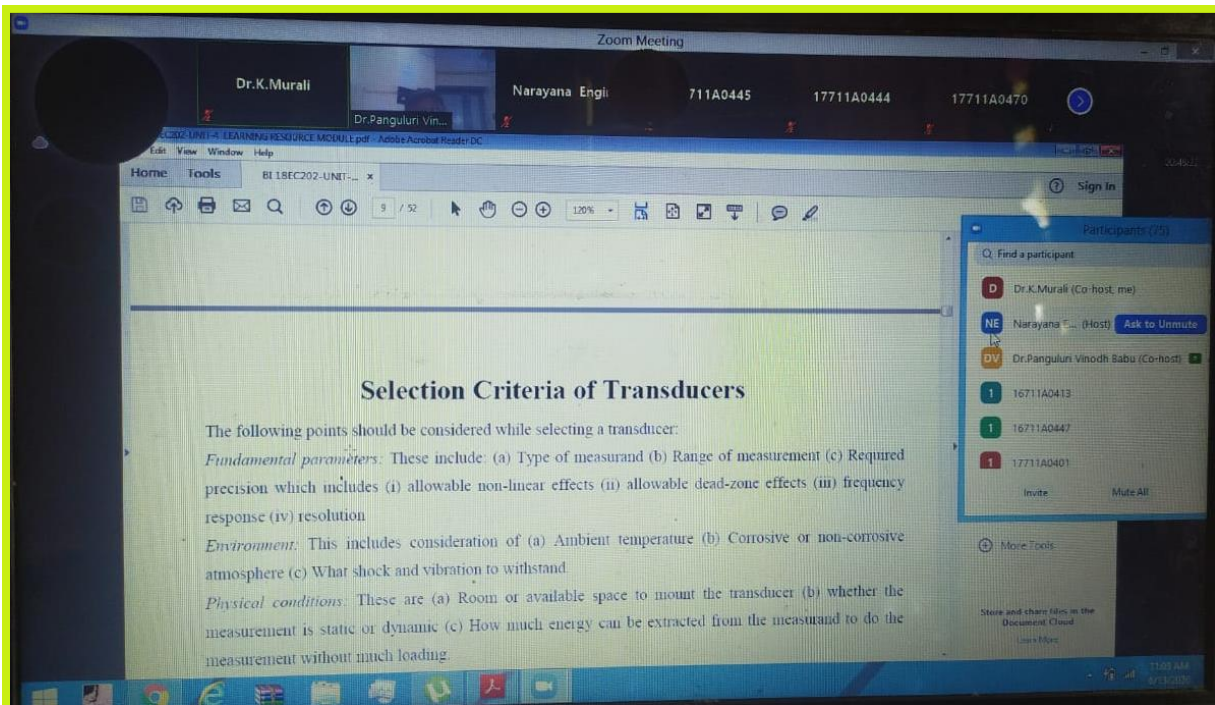


Figure: Explaining the selection criteria of transducers.

At the end of the webinar session, the resource person clarified the queires of students and finally the session was concluded with a vote of thanks.